

1.0A LOW DROPOUT LINEAR REGULATOR

Description

The FL1117-XX is a low-dropout three-terminal regulator with 1.0A output current ability, and the dropout voltage is specified at typical 1.1V at 1.0A current load decreasing at lower load currents.

The FL1117-XX is optimized for low voltage where transient response and minimum input voltage are critical. It provides current limit and thermal shutdown protection solutions. Its circuit includes a trimmed band gap reference to assure output voltage accuracy to be within $\pm 1\%$. On-chip thermal shutdown provides protection against a combination of high current and ambient temperature that would create excessive junction temperature.

The FL1117-XX is available in 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, and 5.0V fixed-output voltage versions and ADJ output voltage version. The fixed versions integrate the adjust resistors.

The FL1117-XX is available in the industry-standard SOT-223 package.

Features

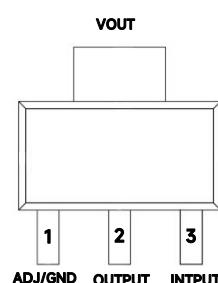
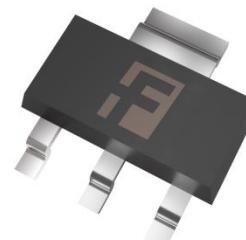
- Provides ADJ Version ($V_{REF} = 1.25V$) and Fixed Voltage 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, and 5.0V with Accuracy $\pm 1\%$ (Except 1.2V)
- Current Limit: 1.3A (Typ.)
- Dropout Voltage: 1.1V (Typ.) @ $I_{OUT} = 1A$
- Regulator Stable with Low ESR MLCC
- Excellent Line Regulation: 0.001%/V (Typ.) @ $I_{OUT} = 30mA$
- Excellent Load Regulation: 0.2%/A @ $I_{OUT} = 1A$
- Quiescent Current: 3.5mA
- Low Output Noise
- PSRR: 70dB
- OTSD Protection
- Operation Junction Temperature: -40°C to +125°C
- Totally Lead-Free & Fully RoHS Compliant
- Halogen- and Antimony-Free. "Green" Device

Marking Informations

Marking	FL1117-12	FL1117-15	FL1117-18	FL1117-25	FL1117-33	FL1117-50	FL1117-AD
VOUT	1.2V	1.5V	1.8V	2.5V	3.3V	5.0V	ADJ

Pin Assignments

SOT-223

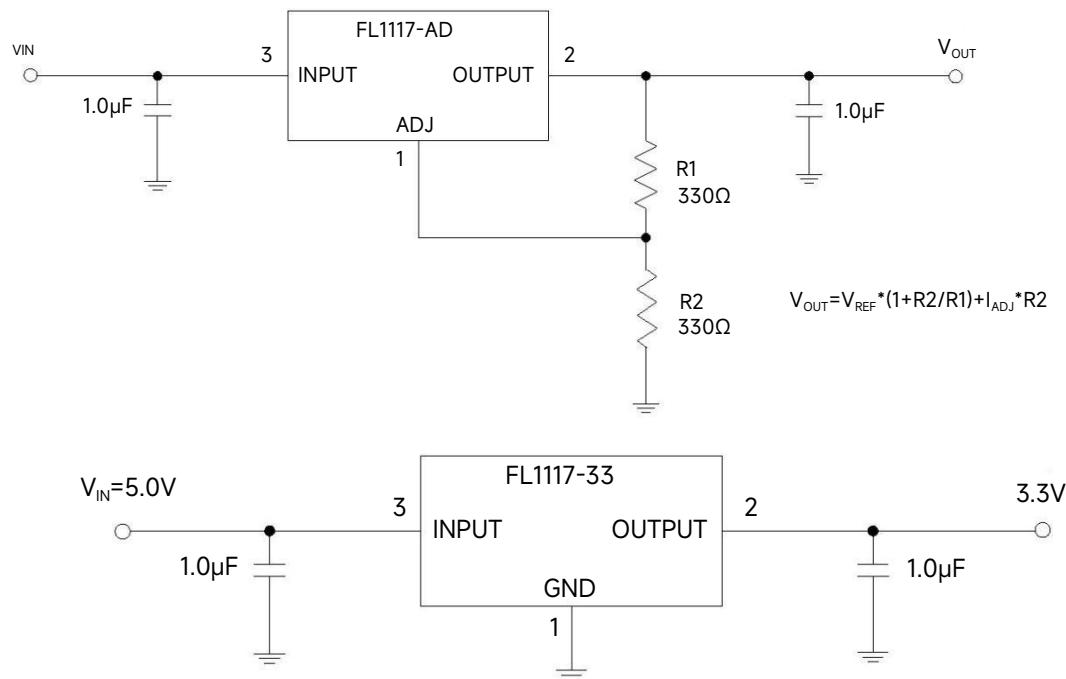


Applications

- TVs and LCD Monitors
- PC Peripherals: Notebooks, Motherboards
- STB

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Typical Applications Circuit (Note 1)



Note: 1. The FL1117-XX is compatible with Low ESR ceramic capacitor. A minimum of $1.0\mu F$ input and output capacitors are required. The ESR of the output capacitors must be less than 1.5Ω .

Close to the OUTPUT pin, it is not recommended to use a capacitor smaller than $0.68\mu F$ in parallel with output capacitor. When the output capacitor parallels $0.1\mu F$ capacitor, the $0.1\mu F$ capacitor must be away from the OUTPUT pin, the distance is no less than 5mm.

Pin Descriptions

Pin Number	Pin Name	Function
1	ADJ/GND	Adjustable Pin or Ground Pin
2	OUTPUT	Regulator Output Pin
3	INPUT	Supply Voltage Pin

Absolute Maximum Ratings (Note 2)

Symbol	Parameter	Rating	Unit
V_{IN}	Power Supply Voltage	16	V
T_J	Operating Junction Temperature Range	+150	°C
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
Θ_{JA}	Thermal Resistance (Junction to Ambient) (Note 3)	65	°C/W
—	ESD (Machine Model)	200	V
—	ESD (Human Body Model)	2000	V

Notes: 2. Stresses greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

3. Chip is soldered to 200mm^2 ($16\text{mm} \times 12.5\text{mm}$) copper (top side solder mask) on 2oz. two layers FR-4 PCB with $8 \times 0.5\text{mm}$ vias.

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Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Supply Voltage	—	13	V
T _J	Operating Junction Temperature Range	-40	+125	°C

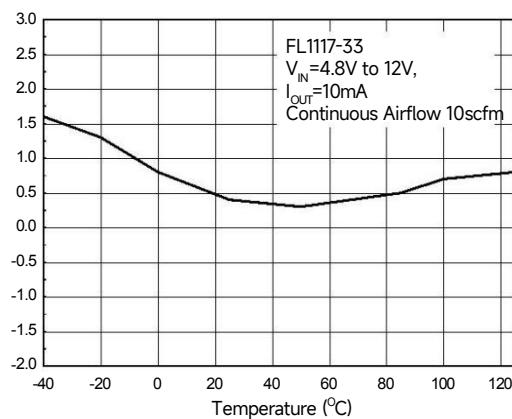
Electrical Characteristics (@ V_{IN} = V_{OUT}+1.5V, C_{IN} = 1.0μF (Ceramic), C_{OUT} = 1.0μF (Ceramic), Typical T_A = +25°C, Bold typeface applies over -40°C ≤ T_J ≤ +125°C ranges, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{REF}	Reference Voltage	V _{OUT} + 1.5V ≤ V _{IN} ≤ 12V, I _{OUT} = 10mA	1.238	1.250	1.262	V
			98% × V _{OUT}	V _{OUT}	102% × V _{OUT}	V
V _{OUT}	Output Voltage (Fixed Versions)	For 1.2V, V _{OUT} + 1.5V ≤ V _{IN} ≤ 12V, I _{OUT} = 10mA	98% × V _{OUT}	V _{OUT}	102% × V _{OUT}	V
			96% × V _{OUT}	V _{OUT}	104% × V _{OUT}	V
		For 1.5V to 5V, V _{OUT} +1.5V ≤ V _{IN} ≤ 12V, I _{OUT} = 10mA	99% × V _{OUT}	V _{OUT}	101% × V _{OUT}	V
			98% × V _{OUT}	V _{OUT}	102% × V _{OUT}	V
V _{DROP}	Dropout Voltage	I _{OUT} = 1A	—	1.1	1.3	V
I _{OUT(MAX)}	Maximum Output Current	1.5V ≤ V _{IN} -V _{OUT}	1	1.3	—	A
V _{RLOAD}	Load Regulation	V _{IN} = V _{OUT} +1.5V 1mA ≤ I _{OUT} ≤ 1A	—	0.2	0.6	%/A
V _{RLINE}	Line Regulation	1.5V ≤ V _{IN} - V _{OUT} ≤ 10V, I _{OUT} = 30mA	—	0.001	±0.04	%/V
I _Q	Quiescent Current	For Fixed Voltage Version, I _{OUT} = 0	—	3.5	6	mA
—	Minimum Load Current	For ADJ Version, 1.5V ≤ V _{IN} -V _{OUT} ≤ 10V	—	2	5	mA
I _{ADJ}	Adjustable Pin Current	—	—	45	90	μA
—	Adjustable Pin Current Change	1.5V ≤ V _{IN} -V _{OUT} ≤ 10V	—	0.2	5	μA
PSRR	Power Supply Rejection Ratio	Ripple 1.0 Vp-p V _{IN} = V _{OUT} + 2V, I _{OUT} = 100mA	—	70	—	dB
			—	70	—	
ΔV _{OUT} / V _{OUT} ΔT	Output Voltage Temperature Coefficient	I _{OUT} = 30mA	—	±30	—	—
V _{NOISE}	RMS Output Noise	10Hz ≤ f ≤ 100kHz, No Load	—	0.003	—	—
T _{OTSD}	Thermal Shutdown Temperature	—	—	+170	—	—
T _{HYOTSD}	Thermal Shutdown Hysteresis	—	—	+20	—	—
Θ _{JC}	Thermal Resistance (Junction to Case)	SOT-223	—	40	—	—

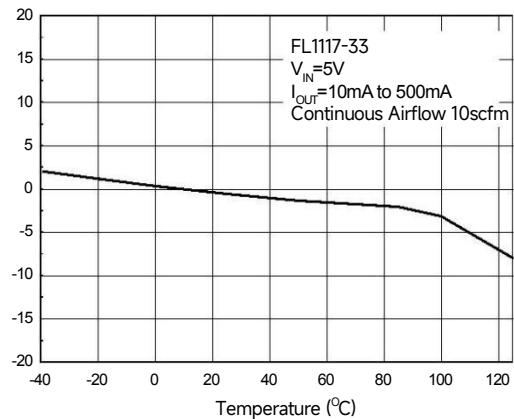
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Performance Characteristics

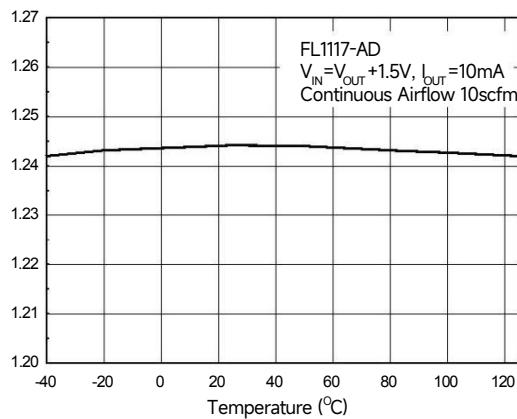
Line Regulation vs. Temperature



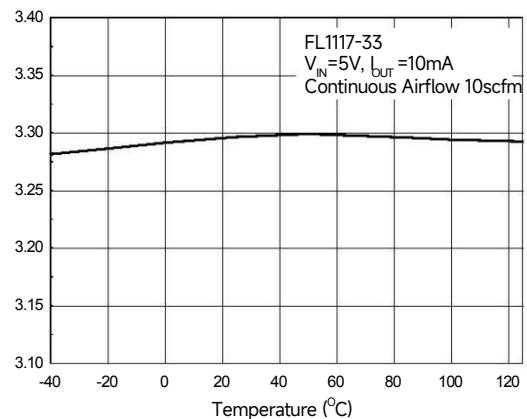
Load Regulation vs. Temperature



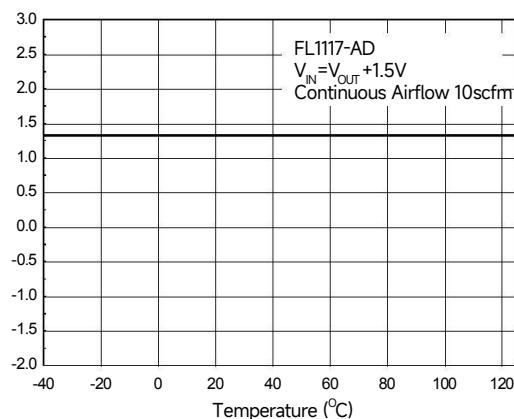
Reference Voltage vs. Temperature



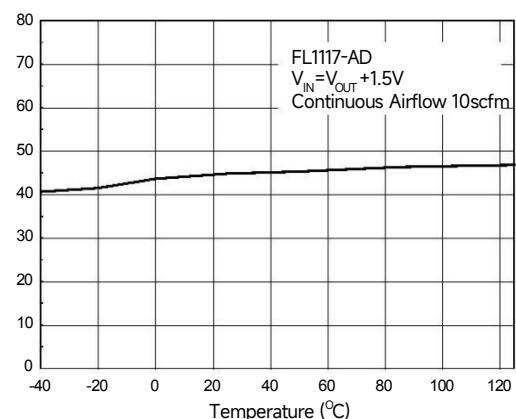
Output Voltage vs. Temperature



Minimum Load Current vs. Temperature



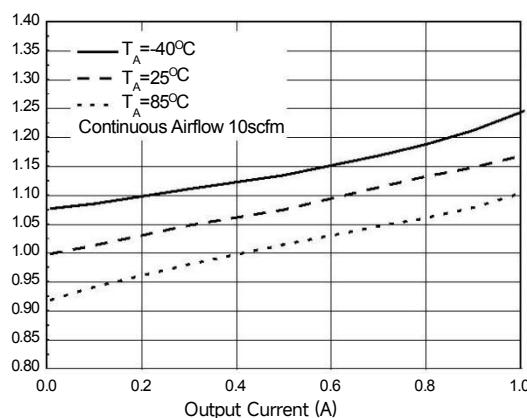
Adjust Pin Current vs. Temperature



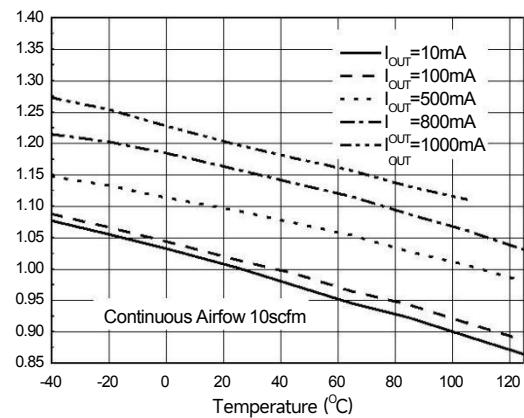
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Performance Characteristics (cont.)

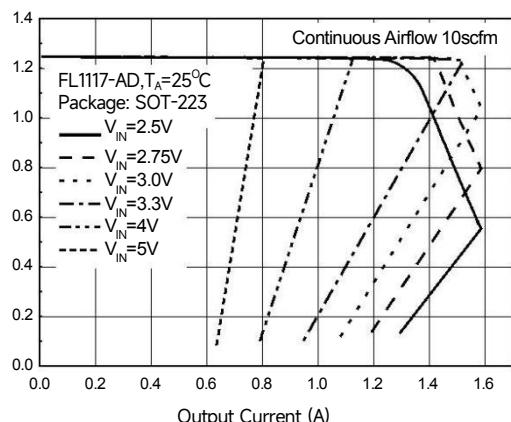
Dropout Voltage vs. Output Current



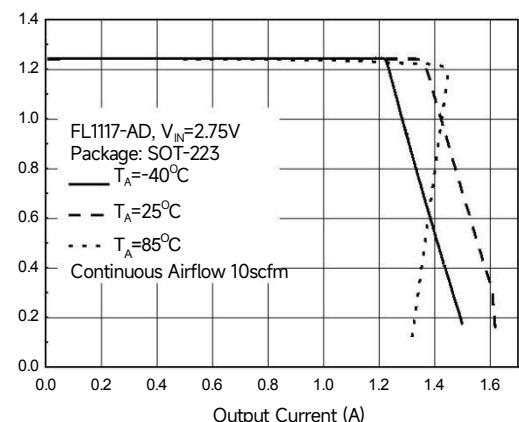
Dropout Voltage vs. Temperature



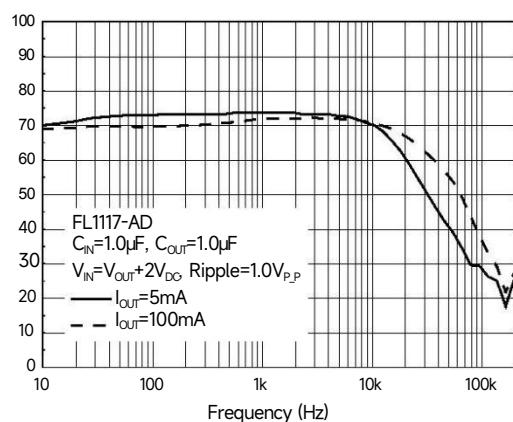
Output Voltage vs. Output Current



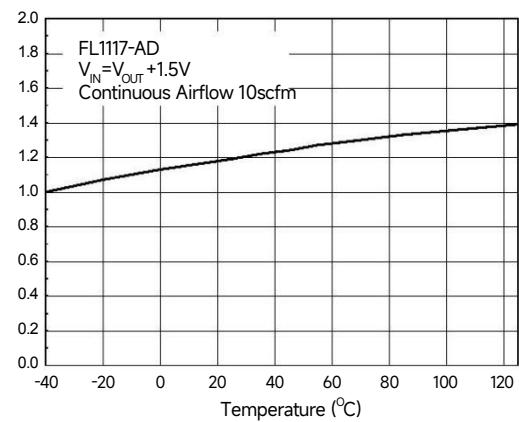
Output Voltage vs. Output Current



PSRR vs. Frequency



Current Limit vs. Temperature



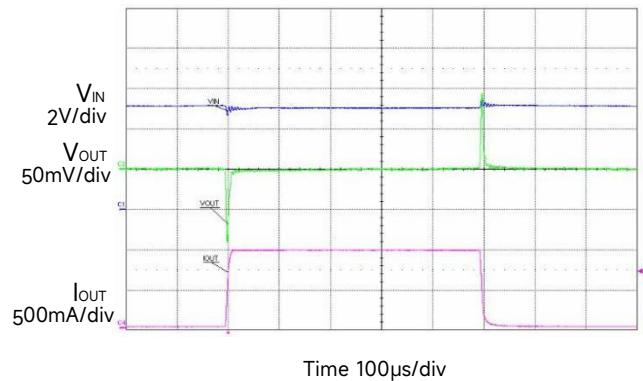
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Performance Characteristics (cont.)

Load Transient Response

(FL1117-AD, $V_{IN}=5V$, $V_{OUT}=3.3V$,

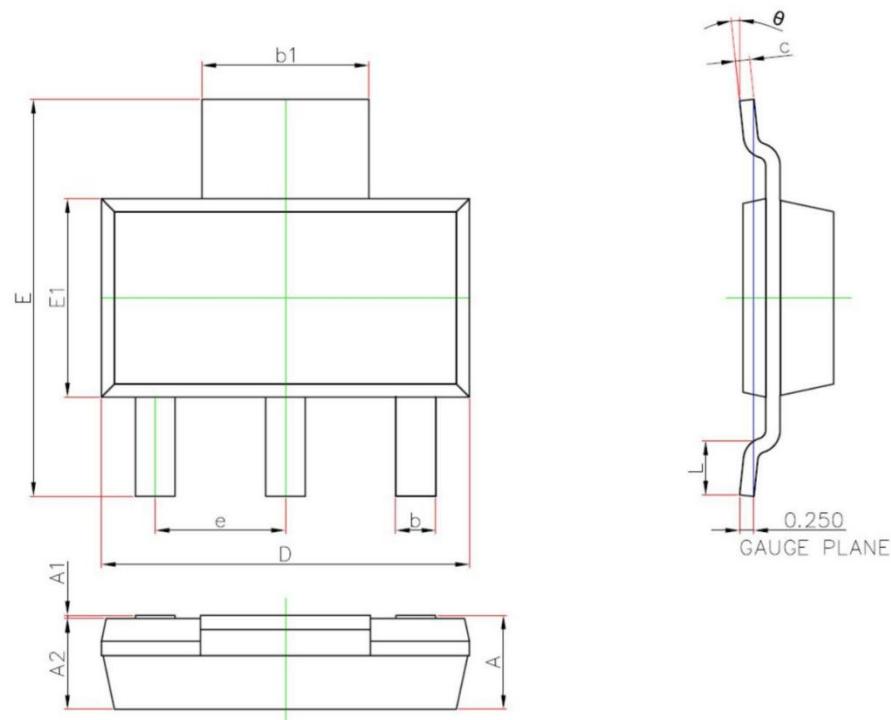
$C_{IN}=1.0\mu F$, $C_{OUT}=1.0\mu F$)



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Package Outline Dimensions

SOT-223



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.800	—	0.071
A1	0.020	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.840	0.026	0.033
b1	2.900	3.100	0.114	0.122
c	0.230	0.350	0.009	0.014
D	6.300	6.700	0.248	0.264
E	6.700	7.300	0.264	0.287
E1	3.300	3.700	0.130	0.146
e	2.300(BSC)		0.091(Bsc)	
L	0.750	—	0.030	—
θ	0°	10°	0°	10°